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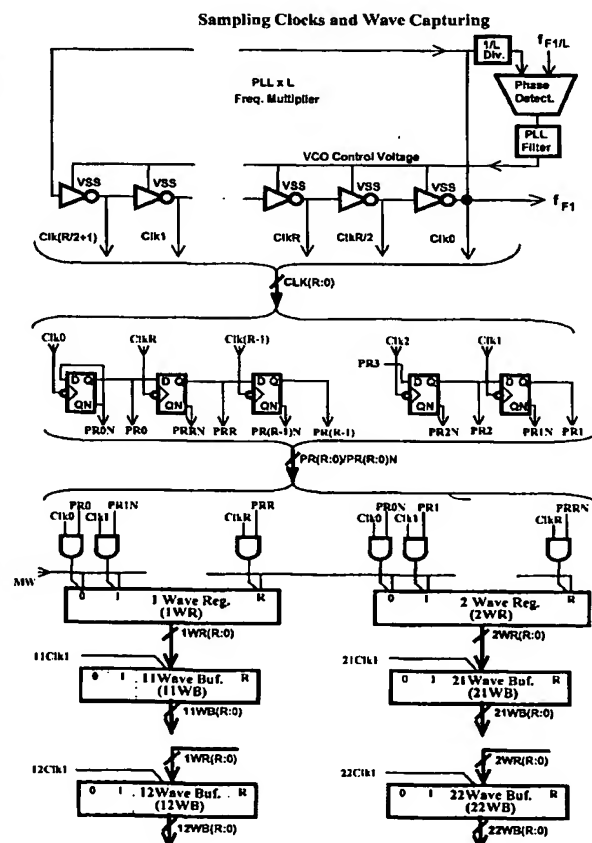
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(54) Title: DIGITAL SIGNAL PROCESSING OF MULTI-SAMPLED PHASE



(57) Abstract: The DSP MSP invention provides an implementation of programmable algorithms for analyzing a very wide range of low and high frequency wave-forms. The DSP MSP comprises a synchronous sequential processor (SSP) for real time capturing and processing of in-coming wave-form and a programmable computing unit (PCU) for controlling SSP operations and supporting adaptive signal analysis algorithms. The DSP MSP further comprises a circuit for Sequential Data Recovery from Multi Sampled Phase (SDR MSP), for a receiver of an optical waveform.

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Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

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